

	Type	L #	Hits	Search Text	DBs	Time Stamp
1	BRS	L1	410	(memory adj module) and ((multi adj chip) multichip)	USPAT; EPO; JPO; DERWEN T; IBM_TD B	2002/07/23 14:30
2	BRS	L2	183	1 and @ay<1997	USPAT; EPO; JPO; DERWEN T; IBM_TD B	2002/07/23 14:10
3	BRS	L3	2804	(memory adj module) and (repair spar\$4 fail\$4)	USPAT; EPO; JPO; DERWEN T; IBM_TD B	2002/07/23 14:32
4	BRS	L4	421	3 and (simmm dimm)	USPAT; EPO; JPO; DERWEN T; IBM_TD B	2002/07/23 14:33
5	BRS	L5	183	4 and @ay<1997	USPAT; EPO; JPO; DERWEN T; IBM_TD B	2002/07/23 14:34
6	BRS	L6	138	5 and chip	USPAT; EPO; JPO; DERWEN T; IBM_TD B	2002/07/23 15:01

	Type	L #	Hits	Search Text	DBs	Time Stamp
7	BRS	L7	3	5315552.pn.	USPAT; EPO; JPO; DERWEN T; IBM_TD B	2002/07/23 15:01